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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

AFTERV

re Applicant:

Brian Taggart et al.

Art Unit:

2818

Serial No.:

10/807.830

Examiner:

Calvin Lee

Filed:

March 24, 2004

Docket:

Assignee:

ITL.1119US P18791

For:

Lower Profile Flexible Substrate Package for Electronic Components

Intel Corporation

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed June 24, 2005, please amend the abovereferenced patent application as follows:

Date of Deposit: _July_6_2005 I hereby optity under 37 GPR 1.8(a) that this correspondence is being deposite with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addregised to the Commissioner for Patents, P.O. Bpx 1450, AkeyAndrija_M_22313-1450, J

Cynthia L Hayder